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IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

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- ☐ 1. **A folded-cascode switched OTA based on current deviation**
Salinas-Cruz, R.; Espinosa-Flores-V, G.;
Devices, Circuits and Systems, 2002. Proceedings of the Fourth IEEE Internat Conference on
17-19 April 2002 Page(s):C024 - C1-4
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[AbstractPlus](#) | Full Text: [PDF](#)(350 KB) IEEE CNF
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- ☐ 2. **Low voltage fully differential opamp with high gain wide bandwidth suital capacitor applications**
Hammouda, S.A.; Tawfik, M.S.; Ragaie, H.F.;
Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Sympos Volume 1, 4-7 Aug. 2002 Page(s):I - 324-7 vol.1
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- ☐ 3. **Switched-capacitor body-biasing technique for very low voltage CMOS ar**
Monsurro, P.; Pennisi, S.; Scotti, G.; Trifiletti, A.;
Circuit Theory and Design, 2005. Proceedings of the 2005 European Conferen Volume 1, 28 Aug.-2 Sept. 2005 Page(s):I/257 - I/260 vol. 1
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- ☐ 4. **Biasing technique via bulk terminal for minimum supply CMOS amplifiers**
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Gunay, Z.S.; Soenen, E.G.; Embabi, S.; Sanchez-Sinencio, E.;
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- ☐ 6. **1.2-V CMOS op-amp with a dynamically biased output stage**
Giustolisi, G.; Palmisano, G.; Segreto, T.;
[Solid-State Circuits, IEEE Journal of](#)
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Digital Object Identifier 10.1109/4.839923

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- ☐ 7. **A low voltage 10Gbps SiGe laser diode driver using adaptive RC compen common-mode feedback based current generators**
Maxim, A.;
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- ☐ 8. **A CMOS class-AB transconductance amplifier for switched-capacitor app**
Rijns, J.J.F.; Wallinga, H.;
[Circuits and Systems, 1990., IEEE International Symposium on](#)
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- ☐ 9. **Fully-differential CMOS current-mode amplifiers and filters**
Zeile, R.H.; Allstot, D.J.; Fiez, T.S.;
[Circuits and Systems, 1991. Conference Proceedings, China., 1991 Internation](#)
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- ☐ 10. **Design of a programmable slew-rate op amp**
Shin, J.M.; Yoon, K.S.;
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- ☐ 11. **Minimization of charge transfer errors in switched-capacitor stages**
Klein, H.-W.; Engl, W.L.;
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- ☐ 12. **Fully-differential CMOS current-mode circuits**
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- ☐ **13. Determination of stability using return ratios in balanced fully differential circuits**
Hurst, P.J.; Lewis, S.H.;
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- ☐ **14. Active current mirror receiver**
Gauthier, C.R.; Brown, R.B.;
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- ☐ **15. An high-swing, 1.8 V, push-pull opamp for sigma-delta modulators**
Malcovati, P.; Maloberti, F.; Terzani, M.;
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- ☐ **16. Fully-differential CMOS current-mode circuits and applications**
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- ☐ **17. A high-performance current mirror using SOI gate controlled hybrid transistors**
Huang, R.; Zhang, X.; Wang, Y.Y.;
[SOI Conference, 1998. Proceedings., 1998 IEEE International](#)
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- ☐ **18. A low-noise low-offset capacitive sensing amplifier for a 50-/spl mu/g/1sp monolithic CMOS MEMS accelerometer**
Jiangfeng Wu; Fedder, G.K.; Carley, L.R.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 39, Issue 5, May 2004 Page(s):722 - 730
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- ☐ **19. Gain and bandwidth boosting techniques for high-speed operational amplifiers**
Amourah, M.M.; Geiger, R.L.;
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- ☐ **20. New low-voltage fully-balanced wide-band differential CMOS current amp**
Bendong Sun; Fei Yuan;
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- ☐ **21. Simple rail-to-rail constant-transconductance input stage operating in st**
Prodanov, V.I.; Green, M.M.;
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- ☐ **22. Integration of cryocooled superconducting analog-to-digital converter an**
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Gupta, D.; Kadin, A.M.; Webber, R.J.; Rochwarger, I.; Bryce, D.; Hollander, W
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- ☐ **23. A fast settling, high DC gain, low power OPAMP design for high resolutio**
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Shahrjerdi, D.; Hekmatshoar, B.; Talaie, M.; Shoaie, O.;
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- ☐ **24. Low-power low-voltage differential class-AB OTAs for SC circuits**
Carvajal, R.G.; Galan, J.; Ramirez-Angulo, J.; Torralba, A.;
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L2	109	1 and emitter adj degeneration	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 10:41
L3	21	2 and (inverse or inversely)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 10:41